



Table of typical power consumption for the DSP Design range of PC/104 Processor and I/O Modules

Module	Function	Power Consumption	Notes
TC486-50	Processor	+5Vdc/660mA	4MB DRAM
TC486-25	Processor	+5Vdc/490mA	4MB DRAM
TC386	Processor	+5Vdc/410mA	4MB DRAM
TSYST	System Controller	+5Vdc/22mA	All digital I/O set to inputs, no floppy or IDE access
TV750	VGA Graphics	+5Vdc/240mA	
TENET	Ethernet Controller	+5Vdc/55mA	Twisted pair or AUI mode
		+5Vdc/120mA	Thin Ethernet mode
TS400	4 x RS232	+5Vdc/80mA	Transceivers active but not connected
		+5Vdc/125mA	Tranceivers looped back
TP406	Digital I/O	+5Vdc/7mA	All I/O set to inputs
TPO24	Digital Output	+5Vdc/5mA	All opto-isolators off
		+5Vdc/55mA	All opto-isolators on
TAD12	Analog I/O	+5Vdc/10mA	
		+12Vdc/10mA	
		-12Vdc/10mA	
TCDEV	Dev. board	+5Vdc/700mA	Accessing floppy, keyboard connected
TFD-01 to TFD-32	Tiny Flash Disks	250mW	Standby
		270mW	Read
		370mW	Write

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